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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: David Alan Edwards, et al.  
Serial No: 09/411,792  
Confirmation No: 8808  
Filed: October 1, 1999  
For: INTERFACE FOR TRANSFERRING DEBUG INFORMATION

Examiner: Ted T. Vo  
Art Unit: 2122

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**REQUEST FOR RECONSIDERATION**

Sir:

In response to the Office Action mailed June 2, 2005, Applicants respectfully request reconsideration. Claims 1-64 are currently pending and the application as presented is believed to be in condition for allowance.

The Office Action rejects claims 1-64 under 35 U.S.C. §102(b) as purportedly being unpatentable over Circello (5,737,516). Applicants respectfully traverse this rejection.

Initially, Applicants incorporate herein by reference the arguments made in the response filed on April 6, 2005. In particular, in the response filed April 6, 2005, Applicants argued, “[a]pplicants respectfully disagree that an operand address can be interpreted as an instruction address. During the telephone interview, Applicants pointed out that although Circello may disclose sending instruction addresses from the core 9 to the debug circuit 10 (e.g., via the KADDR signal), an instruction address is very different from an operand address. An instruction address specifies the memory location of an instruction, while an operand address specifies the